

## Appendix B

## VME64x Signal/Pin Descriptions

## Introduction

This appendix describes the additional VME64x signal lines and associated connector pin defined in this standard. See the VME64 Standard, Appendix B for the VME64 signal/pin descriptions. The following table identifies the VME64x signal/pin by mnemonic and describes the signal/pin characteristics.

## VME64 Signal Identification

Signal/Pin Mnemonic	Signal/Pin Name and Description
GA[4..0]*	Geographical Address - A set of backplane driven signals that are either tied to ground or floating and are used to carry slot number information.
GAP*	Geographical Address Parity - A backplane driven signal that is either tied to ground or floating and is used to carry geographical address parity.
GND	The DC voltage reference and to carry the power return current for boards and backplanes.
LI/I*	Live Insertion Input - A three-state driven signal that is used to carry hot swap (live insertion) input control information.
LI/O*	Live Insertion Output - A three-state driven signal that is used to carry hot swap (live insertion) output control information.
MCLK	IEEE 1149.5 MTM-Bus Clock - A three-state driven signal that is used to carry the T&Mbus clock signal.
MCTL	IEEE 1149.5 MTM-Bus Control - A three-state driven signal that is used to carry the T&Mbus control signal.
MMD	IEEE 1149.5 MTM-Bus Master Data - A three-state driven signal that is used to carry the T&Mbus master data signal information.
MPR	IEEE 1149.5 MTM-Bus Pause Request - A three-state driven signal that is used to carry the T&Mbus pause request signal information.
MSD	IEEE 1149.5 MTM-Bus Slave Data - A three-state driven signal that is used to carry the T&Mbus slave data signal information.
RESP*	Response - A three-state driven signal that is used to carry the Response signal information as defined by the 2eVME Protocol.
RsvB	Reserved Bused - These reserved bused signals are reserved for future definition by the VITA Standards Organization.
RsvU	Reserved Unbused - These connector pins are reserved for future definition by the VITA Standards Organization.

**Appendix B****VME64x Signal/Pin Descriptions****Signal/Pin  
Mnemonic****Signal / Pin Name and Description**

UD	User Defined - These pins are defined by the user and are used to carry various types of input/output information.
VPC	Voltage PreCharge - A power voltage used by the hot swap logic.
+3.3V	+3.3 VDC Power - Used by on board logic circuits.
+V1	The positive side of the 48 volt supply, usually paired with the +V2 power pin.
+V2	The positive side of the 48 volt supply, usually paired with the +V1 power pin.
-V1	The negative side of the 48 volt supply, usually paired with the -V2 power pin.
-V2	The negative side of the 48 volt supply, usually paired with the -V1 power pin.